

Q1  
forming the organic insulating film on the transparent substrate to a thickness of between 0.8 $\mu$ m and 1.5 $\mu$ m, the organic insulating film being provided on the data line; and forming the pixel electrode on the organic insulating film so as to be overlapped, by a predetermined area, with at least one of the gate line and the data line.

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Q2  
13. (AMENDED) A liquid crystal display device including a thin film transistor formed at an intersection between a gate line and a data line, and a pixel electrode connected to a source electrode of the thin film transistor and overlapped with at least one of the gate line and the data line with having an organic insulating film therebetween and being provided on the data line, wherein a thickness and a dielectric constant of the organic insulating film are selected such that a signal delay is less than 10  $\mu$ sec for each of the gate lines and the data line.

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Q3  
19. (AMENDED) The liquid crystal display device according to claim 13, wherein the dielectric constant of the organic insulating film is less than 3.0.

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Q4  
22. (AMENDED) A liquid crystal display device including a thin film transistor formed at an intersection between a gate line and a data line, and a pixel electrode connected to a source electrode of the thin film transistor and overlapped with at least one of the gate line and the data line with having an organic insulating film therebetween and being provided on the data line, wherein a thickness of the organic insulating film is between 0.8 $\mu$ m and 1.5 $\mu$ m.

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